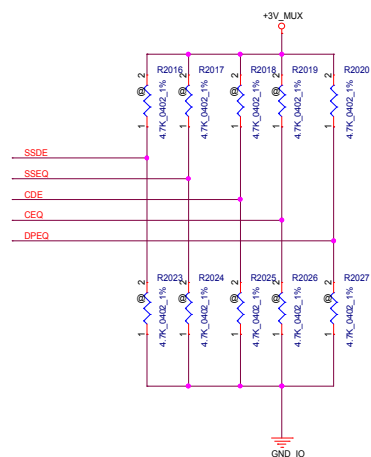



PSS8740 Mode Selection		
SW2	SW1	SW0
L	L	L
L	L	R
L	R	L
L	R	R
R	L	L
R	L	R
R	R	L
R	R	R

## PS8740A Pin Control Mode



```
DP Receiver equalization setting; Internal tied to VDD33/2, 3.3V I/O.
DPEQ =
L: Programmable EQ level#1
H: Programmable EQ level#2
M: Programmable EQ level#3 (default)
```



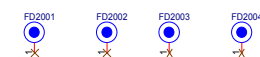
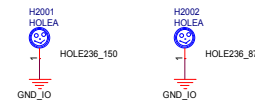
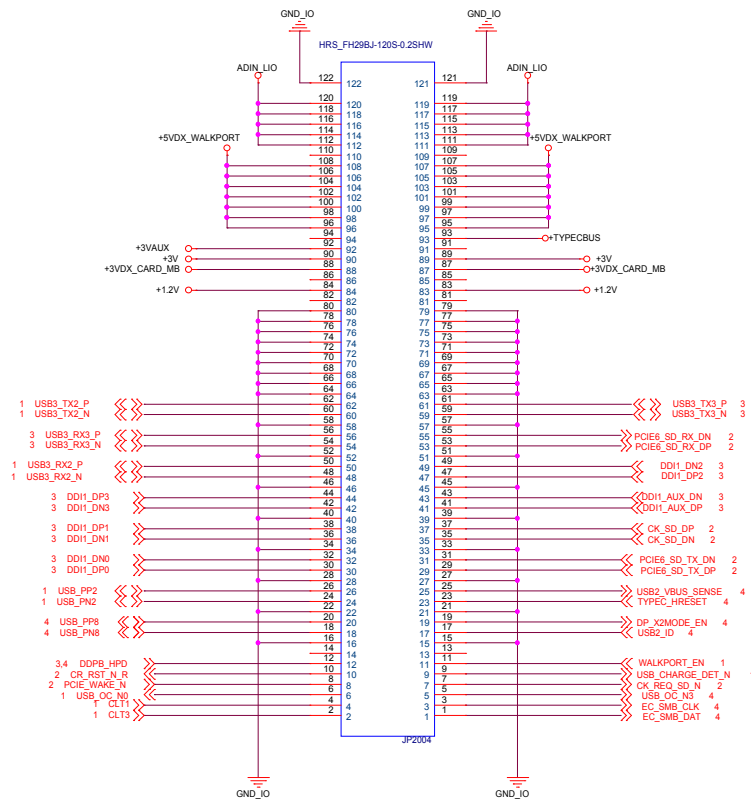
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